



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

First Named Inventor: NISHIZAWA, Hirotaka

Art Unit: 2876

Appln. No.: 10/550,559

Examiner: LABAZE, E.

Filed: October 20, 2005

Confirmation No.: 1908

For: SEMICONDUCTOR DEVICE

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

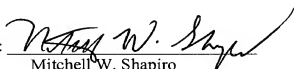
Document 1 is the laid-open publication of the Japanese Application cited on page 3 of the specification. Documents 2-3 were cited in the International Search Report (copy attached) and the International Preliminary Report on Patentability (copy attached).

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10437) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

Date: November 6, 2007

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